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Spotlight

SEMICON 2.0 New Push to Strengthen India's Semiconductor Ecosystem



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Semicon 2.0: New Push to Strengthen India's Semiconductor Ecosystem | 16 July

ANI
Broadcast



Semicon 2.0 Approved: What's Next for India's Semiconductor Ambitions? | Ashok Chandak,

POWERED BY CYPHER

ASHOK CHANDAK ON INDIA'S CHIP REVOLUTION
CAN IT BE A CHIP HUB?

ASHOK CHANDAK
CEO AND PRESIDENT, ISA & SEMI-INDIA

SUDHI SACHDEV
AIM

PLAY CONT PAGE

Students | Uber Co... Put at least we are on a16z | Cypher 2026 India's Biggest AI Summit and Exp

Semicon 2.0 will make India a global hub, say experts

SECTOR LIFT. Industry expects investment, R&D, domestic value addition to accelerate

S Romendra Singh
New Delhi

The approval of the next phase of the Semicon India Programme (Semicon2.0), with an outlay of ₹27 lakh crore, has the potential to position India not just as a manufacturing destination, but as a globally competitive hub for semiconductor innovation, said analysts and industry veterans.

The Cabinet on Wednesday approved the policy under the India Semiconductor Mission (ISM) to accelerate the development of a robust semiconductor ecosystem.

INDUSTRY CATALYST
“Semicon 2.0 marks a decisive step in India’s journey from policy intent to execution at scale. Phase 1 established India’s credibility; Phase 2 builds long-term capability. By strengthening ATMP/OSAT packaging, R&D, and talent development, each backed by incentives of up to 75 per cent under the design and R&D pillars, the government is creating the full-spectrum support environment for and



TECH LEAP. With an outlay of ₹1.27 lakh crore, Semicon 2.0 seeks to strengthen India’s global semiconductor footprint

foundation for a globally competitive ecosystem that will attract sustained investments, create high-value employment and enhance domestic value addition,” Ashok Chandra, President, India Electronics and Semiconductor Association IESA and SEMI India (Indian arm of the global semiconductor industry association), said.

He said Semicon 2.0 comes at an opportune time, as the global semiconductor industry is entering an unprecedented investment cycle. “With worldwide semiconductor manufacturing equipment spending expected to grow to nearly \$230 billion by 2028, the policy positions India to capture the next wave of global

investments,” he added. According to IESA, the ISM 2.0 is also expected to attract another \$40-50 billion in investments, create 2-3 lakh high-skilled jobs, significantly strengthen India’s position as a trusted global semiconductor partner.

With complementary initiatives such as the Electronics Components Manufacturing Scheme (ECMS), Production Linked Incentive (PLI), Electronics Manufacturing Clusters (EMC 2.0) and State programmes, Semicon 2.0 has the potential to accelerate India’s journey towards nearly 40 per cent domestic value addition in electronics manufacturing, it added.

Pankaj Mohindroo, Chairman, India Cellular & Electronics Association (ICEA), said India has demonstrated the intent and long-term commitment to the industry with more than 12 approved projects in just four years, under ISM 1.0 and ISM 2.0 now builds on this strong foundation with an emphasis on design, R&D, capital goods, and the entire supply chain and skill development.

“This reflects the government’s long-term vision to build capabilities that are relevant to global supply chains and position India as a consequential player in the global semiconductor industry. Forward-looking State governments will now fiercely compete for investments to get a share of ISM 2.0. Semiconductor design, including AI compute with IP owned by Indian companies, will be a focus area,” Mohindroo said.

Aisha Ali Hussaini, Semiconductor Tax Partner at EY India, said, “With anchor investments secured in fabrication & packaging and commercial production now underway, India has moved decisively from ambition to execution.”

Tech, capital hurdles for road map to 2 nm chip: industry

QASVI GUPTA
New Delhi, July 26

WHILE FORESEEING A planned goal to move up the technology curve from the 28 nanometre (nm) chips currently being planned domestically to 7 nm, and eventually to 2 nm, industry executives said the leap will require solving four critical challenges—technology transfer, capital, talent and access to advanced lithography equipment.

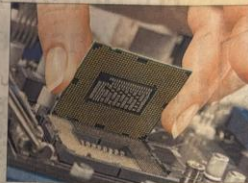
Tata Electronics, whose upcoming Dholera fab represents a double-digit-billion-dollar investment, is currently building capacity only in the 28 nm to 110 nm range. With Taiwan’s PSMC as a technology partner, the firm is putting India’s most advanced committed project well short of the nodes the government has eyed as long-term goals in India Semiconductor Mission 2.0.

Hareesh Chandrasekar, CEO and co-founder of AGNI Semiconductors, said any realistic move to advanced nodes hinges on several factors—PLI, Electronics Manufacturing Clusters (EMC) where the technology comes from, how much capital it requires, and whether India has the skilled manpower to execute at scale. India might want to take lead from Japan’s Rapidus, which has licensed

THE BOTTLENECKS

■ India’s priority should be deepening the broader chain before racing toward a specific node number, experts said

■ For this, the strategy rests on organic research rather than acquiring foreign technology outright, they said



IBM’s process technology and is targeting 2 nm production directly skipping intermediate nodes, he said.

On lithography, Chandrasekar said EUV access could prove to be the harder constraint, potentially requiring government-to-government engagement rather than a standard commercial order.

Meanwhile, Navin Bishnoi, chairman of the India Electronics and Semiconductor Association (IESA), said only three companies worldwide currently operate at the most advanced process nodes, with Japan’s Rapidus the newest entrant attempting to join that group.

“The ecosystem depth matters more rather than a hurry to go to an advanced node,” Bishnoi said, adding that India’s priority should be deepening the broader chain—design, packaging, materials, equipment and IP—before racing toward a specific node number.

An outright technology purchase would offer little return, given India is not yet an advanced manufacturing base. S. Krishnan, Ministry of Electronics and Information Technology (MeitY) secretary, said the strategy rests on organic research rather than acquiring foreign technology outright.

■ Costs roughly double at each node, from 28 nm to 14 nm, to 7 nm, to 3-4 nm

■ Only three companies worldwide currently operate at the most advanced process nodes

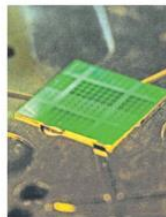
India’s oldest chip fab reboot awaits a final green light

Jatin Grover & Shouvik Das
NEW DELHI

Even amid its rush to build a domestic semiconductor ecosystem, India’s plan to revive its oldest and only state-owned chip fabrication plant—ravaged by a fire nearly four decades ago—remains stuck. Six months after the government selected three private firms to modernize the Semiconductor Laboratory (SCL) in Mohali, the project is still awaiting key approvals and other decisions, government officials and industry stakeholders said.

On 4 December 2025, Tata Semiconductor Manufacturing, Cyient Semiconductors and Applied Materials emerged as the preferred bidders for SCL’s ₹4,500 crore (\$470 million) modernization project.

Mint’s conversations with four officials and industry stakeholders with direct knowledge of the matter revealed a mix of factors



The proposal would require the Union cabinet’s nod. **APF**

weighing on India’s ambitions to revive the semiconductor fabrication plant.

One of the factors, an official said, is that the proposal would require the Union cabinet’s nod.

“The government is in the process of submitting the necessary paperwork for the Cabinet’s approval,” another senior official directly associated with the approvals told Mint. “The December selection of bidders was to finalize the tender pro-

cess. We are now in process of submitting the due application to the Cabinet, for approval,” the official said, without specifying a timeline for the same.

This approval is crucial for the SCL project to be given its requisite contracts, a third official said.

To be sure, while India is now going all out to catch up, SCL Mohali had been established in 1984, a good three years before global leader Taiwan Semiconductor Manufacturing Co. was founded. The project was designed to make the country a key stakeholder of the semiconductor supply chain that was rising in Asia, as US giants Intel, IBM, Fairchild Semiconductor and Texas Instruments raced to design chips at the boom of personal computing.

However, a mysterious fire in 1989 ravaged most of SCL’s machinery, and the fab unit never reached the potential it could have.

TURN TO PAGE 6

IESA Welcomes Launch of SEMICON 2.0

Bengaluru: The India Electronics and Semiconductor Association (IESA) welcomes today's Union Cabinet decisions, which together commit an outlay of Rs.2,19,353 crore across infrastructure and electronics manufacturing, including two significant announcements for India's semiconductor and electronics industry - Semicon 2.0, approved at an outlay of Rs.1,27,500 crore, and the new Mobile Phone Manufacturing Scheme (MPMS), approved at an outlay of Rs.62,500 crore over five years. Ashok Chandak, President, IESA and SEMI India, said, SEMICON2.0 marks decisive step in India's journey from policy intent to execution at scale. Phase 1 established India's credibility; Phase 2 builds long-term capability. SEMICON India2.0 comes at an opportune moment as the global semiconductor industry enters an unprecedented investment cycle. Semicon India 2.0 will strengthen technological self-reliance and establish India as one of the world's most trusted semiconductor and electronics manufacturing destinations, commented Mr. Navin Bishnoi, Chairperson, IESA.

ଆଇଇଏସଏ ସେମିକନ୍ ଇଣ୍ଡିଆ ୨.୦କୁ ସ୍ବାଗତ- ଭାରତକୁ ଏକ ବିଶ୍ୱସ୍ତରୀୟ ସେମିକଣ୍ଡକ୍ଚର ହବ୍ କରିବା ଦିଗରେ ବଡ଼ ପଦକ୍ଷେପ

ବେଙ୍ଗାଲୁରୁ: ଇଣ୍ଡିଆ ଇଲେକ୍ଟ୍ରୋନିକ୍ ଆଣ୍ଡ ସେମିକଣ୍ଡକ୍ଚର ଆସୋସିଏସନ୍, କେନ୍ଦ୍ର ମନ୍ତ୍ରୀମଣ୍ଡଳ ପକ୍ଷରୁ ଅନୁମୋଦିତ ସେମିକନ୍ ଇଣ୍ଡିଆ ୨.୦ କାର୍ଯ୍ୟକ୍ରମକୁ ସ୍ବାଗତ କରିଛି । ଏହା ସହିତ ଇତିମୁଖି ଓ ଇଲେକ୍ଟ୍ରୋନିକ୍ ଉତ୍ପାଦନ କ୍ଷେତ୍ର ପାଇଁ ମୋଟ ୨ ଲକ୍ଷ ୧୯ ହଜାର ୩୫୩ କୋଟି ଟଙ୍କାର ବ୍ୟୟବରାଦକୁ ମଧ୍ୟ ସଂଗଠନ ପ୍ରଶଂସା କରିଛି । ଏହି ପ୍ୟାକେଜ୍ ଅନ୍ତର୍ଗତ ୧ ଲକ୍ଷ ୨୭ ହଜାର ୫୦୦ କୋଟି ଟଙ୍କା ବ୍ୟୟରେ ସେମିକନ୍ ଇଣ୍ଡିଆ ୨.୦

ଏବଂ ପାର୍ବର୍ଷ ପାଇଁ ୬୨ ହଜାର ୫୦୦ କୋଟି ଟଙ୍କା ବ୍ୟୟରେ ମୋବାଇଲ୍ ଫୋନ୍ ମ୍ୟାନ୍ୟୁଫ୍ୟାକ୍ଚରିଂ ଷ୍ଟିମ୍ ଲୁ ଫୁଲ ଅନୁମୋଦନ ମିଳିଛି । ଏହି ଅବସରରେ ଆଇଇଏସଏ ଓ ସେମି ଇଣ୍ଡିଆର ସମାପ୍ତି ଅଶୋକ ଚାନ୍ଦକ କହିଛନ୍ତି ଯେ, ସେମିକନ୍ ଇଣ୍ଡିଆ ୨.୦ ଭାରତର ସେମିକଣ୍ଡକ୍ଚର ଯାତ୍ରାରେ ଏକ ନିର୍ଣ୍ଣାୟକ ପଦକ୍ଷେପ ଅଟେ । ପ୍ରଥମ ପର୍ଯ୍ୟାୟ ଭାରତର ବିଶ୍ୱବ୍ୟାପୀୟତାକୁ ପ୍ରତିଷ୍ଠା କରିଥିବା ବେଳେ, ଦ୍ୱିତୀୟ ପର୍ଯ୍ୟାୟ ବାର୍ଦ୍ଧିଆପି

Bengaluru : The India Electronics and Semiconductor Association (IESA) welcomes today's Union Cabinet decisions, which together commit an outlay of Rs. 2,19,353 crore across infrastructure and electronics manufacturing, including two significant announcements for India's semiconductor and electronics industry - Semicon 2.0, approved at an outlay of Rs. 1,27,500 crore, and the new Mobile Phone Manufacturing Scheme (MPMS), approved at an outlay of Rs. 62,500 crore over five years. Building on the strong foundation and momentum laid under Phase 1 of India Semiconductor Mission, the programme marks government's commitment on long-term policy support and India's transition from establishing semiconductor manufacturing to building a comprehensive and globally competitive ecosystem spanning fabs, advanced packaging, design, R&D, talent, machines (equipment), materials and supply chains. Phase 1 has established India's credibility by attracting over US\$20 billion in announced semiconductor investments and enabling multiple Fab and ATMP/OSAT projects. With several OSAT projects already under implementation and entering production, Phase 2 rightly places greater emphasis on semiconductor fabs, while continuing strong support for advanced packaging, thereby creating a balanced and future-ready semiconductor ecosystem. Ashok Chandak, President, IESA and SEMI India, said: "SEMICON 2.0 marks decisive step in India's journey from policy intent to execution at scale. Phase 1 established India's credibility; Phase 2 builds long-term capability. By strengthening fabs, advanced packaging, design, R&D, talent and the semiconductor supply chain, the Government is creating the foundation for a globally competitive ecosystem that will attract sustained investments, create high-value employment and significantly enhance domestic value addition. SEMICON India 2.0 comes at an opportune moment as the global semiconductor industry enters an unprecedented investment cycle. With worldwide semiconductor manufacturing equipment spending expected to grow to nearly US\$30 billion by 2028, the policy positions India to capture the next wave of global investments in semiconductor manufacturing, advanced packaging, equipment and materials, Chandak added. "Equally significant is the approval of the Mobile Phone

Manufacturing Scheme (MPMS) that is designed to take mobile manufacturing to its next phase, with an ambition to nearly double cumulative production to about Rs. 39 lakh crore and cumulative exports to about Rs. 15 lakh crore, while creating 60,000 additional direct jobs. Given that India has already manufactured approximately 125 crore mobile phones emerging as the country's second-largest mobile manufacturing ecosystem place, and now needs 99.2 percent of domestic demand through 'Made in India' devices, this scheme provides the continuity and ambition needed to sustain that momentum, while its incentives for domestic sourcing and for design and R&D by Indian brands will help deepen value addition and build genuine Indian intellectual property." Chandak added. "These decisions also validate the broader emphasis on Design and R&D. The enhanced focus on indigenous product development, industry-led R&D and innovation will help transform India from a design powerhouse into a complete design-to-manufacturing destination. Together with complementary initiatives, Semicon India 2.0 will strengthen technological self-reliance and establish India as one of the world's most trusted semiconductor and electronics manufacturing destinations, commented Mr. Navin Bishnoi, Chairperson, IESA. The programme is expected to catalyse another US\$40-50 billion in investments, create 2-3 lakh high-skilled jobs, significantly enhance domestic value addition and strengthen India's position as a trusted global semiconductor partner. Together with complementary initiatives such as the Electronics Components Manufacturing Scheme (ECMS), Production Linked Incentive (PLI), Electronics Manufacturing Clusters (EMC, 2.0) and state programmes, SEMICON 2.0 has the potential to accelerate India's journey towards nearly 40% domestic value addition in electronics manufacturing, bringing the nation significantly closer to the vision of a truly Atmanirbhar Bharat. The India Electronics and Semiconductor Association (IESA) has welcomed the launch of SEMICON India 2.0, describing it as a landmark initiative that will strengthen India's position as a trusted global semiconductor hub. Congratulating the Government of India, the Ministry of Electronics and Information Technology (MeitY) and the India Semiconductor Mission (ISM), IESA

said the programme lays the foundation for a comprehensive semiconductor ecosystem spanning research, design, manufacturing, packaging, equipment, materials and talent development. The initiative places a strong emphasis on expanding semiconductor fabrication facilities to reduce strategic import dependence, attract global investments and complete the country's semiconductor value chain. Continued support for Outsourced Semiconductor Assembly and Test (OSAT) and advanced packaging is expected to create high-value manufacturing jobs while positioning India as a preferred destination for chiplets and heterogeneous integration technologies critical for the AI era. SEMICON India 2.0 also prioritises talent development through specialised education, industry-academia collaboration and skill-building programmes aimed at creating more than two lakh skilled professionals to meet the projected global demand for nearly one million semiconductor professionals by 2030. Enhanced support under the Design Linked Incentive (DLI) Scheme is expected to accelerate indigenous chip design, intellectual property creation and semiconductor product innovation, enabling Indian startups to undertake advanced chip development programmes. The initiative further highlights the importance of industry-driven, market-oriented research and development to commercialise indigenous technologies and strengthen India's design-to-manufacturing capabilities. In addition, a dedicated focus on semiconductor equipment, specialty chemicals, gases, materials, consumables and precision engineering will deepen domestic value addition, create opportunities for MSMEs and reinforce India's role in global semiconductor supply chains. Welcoming the initiative, IESA Vice Chairperson Pradeep Vajram said the association looks forward to working closely with MeitY, the India Semiconductor Mission, state governments, industry, academia and global partners to ensure the successful implementation of SEMICON India 2.0. He said IESA will continue supporting workforce development, startup growth, industry-led R&D, policy advocacy and international collaborations to build a resilient, innovation-driven and globally competitive semiconductor ecosystem aligned with India's vision of technological leadership and Atmanirbhar Bharat.

Pratadin

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ବେଙ୍ଗାଲୁରୁ: ଇଣ୍ଡିଆ ଇଲେକ୍ଟ୍ରୋନିକ୍ ଆଣ୍ଡ ସେମିକଣ୍ଡକ୍ଚର ଆସୋସିଏସନ୍, କେନ୍ଦ୍ର ମନ୍ତ୍ରୀମଣ୍ଡଳ ପକ୍ଷରୁ ଅନୁମୋଦିତ ସେମିକନ୍ ଇଣ୍ଡିଆ ୨.୦ କାର୍ଯ୍ୟକ୍ରମକୁ ସ୍ବାଗତ କରିଛି । ଏହା ସହିତ ଇତିମୁଖି ଓ ଇଲେକ୍ଟ୍ରୋନିକ୍ ଉତ୍ପାଦନ କ୍ଷେତ୍ର ପାଇଁ ମୋଟ ୨ ଲକ୍ଷ ୧୯ ହଜାର ୩୫୩ କୋଟି ଟଙ୍କାର ବ୍ୟୟବରାଦକୁ ମଧ୍ୟ ସଂଗଠନ ପ୍ରଶଂସା କରିଛି । ଏହି ପ୍ୟାକେଜ୍ ଅନ୍ତର୍ଗତ ୧ ଲକ୍ଷ ୨୭ ହଜାର ୫୦୦ କୋଟି ଟଙ୍କା ବ୍ୟୟରେ ସେମିକନ୍ ଇଣ୍ଡିଆ ୨.୦ ଏବଂ ପାର୍ବର୍ଷ ପାଇଁ ୬୨ ହଜାର ୫୦୦ କୋଟି ଟଙ୍କା ବ୍ୟୟରେ ମୋବାଇଲ୍ ଫୋନ୍ ମ୍ୟାନ୍ୟୁଫ୍ୟାକ୍ଚରିଂ ଷ୍ଟିମ୍ ଲୁ ଫୁଲ ଅନୁମୋଦନ ମିଳିଛି । ଏହି ଅବସରରେ ଆଇଇଏସଏ ଓ

ସେମିକନ୍ ଇଣ୍ଡିଆ ୨.୦ ଭାରତର ସେମିକଣ୍ଡକ୍ଚର ଯାତ୍ରାରେ ଏକ ନିର୍ଣ୍ଣାୟକ ପଦକ୍ଷେପ ଅଟେ । ପ୍ରଥମ ପର୍ଯ୍ୟାୟ ଭାରତର ବିଶ୍ୱବ୍ୟାପୀୟତାକୁ ପ୍ରତିଷ୍ଠା କରିଥିବା ବେଳେ, ଦ୍ୱିତୀୟ ପର୍ଯ୍ୟାୟ ବାର୍ଦ୍ଧିଆପି କ୍ଷମତା ବିକାଶ ଉପରେ ଗୁରୁତ୍ୱ ଦେବ । ବିଶ୍ୱ ସେମିକଣ୍ଡକ୍ଚର ଶିଖର ବୁହର୍ ବିନିଯୋଗର ସୁଯୋଗ ସୃଷ୍ଟି ହେଉଥିବା ସମୟରେ ଏହି ଯୋଜନା ଅତ୍ୟନ୍ତ ସମୟୋପଯୋଗୀ ହେବ । ଆଇଇଏସଏର ଅଧ୍ୟକ୍ଷ ନବୀନ ବିଶ୍ୱନୋଇ କହିଛନ୍ତି ଯେ, ସେମିକନ୍ ଇଣ୍ଡିଆ ୨.୦ ଭାରତର ପ୍ରଯୁଗ୍ମିତ ଆତ୍ମନିର୍ଭରତାକୁ ସୁଦୃଢ଼ କରିବା ସହ ଦେଶକୁ ବିଶ୍ୱର ସେମିକଣ୍ଡକ୍ଚର ଓ ଇଲେକ୍ଟ୍ରୋନିକ୍ ଉତ୍ପାଦନ କ୍ଷେତ୍ରରେ ଗୁରୁତ୍ୱପୂର୍ଣ୍ଣ ଭୂମିକା ଗ୍ରହଣ କରିବ ।

designed to take mobile manufacturing to its next phase, with an ambition to nearly double cumulative production to about Rs. 39 lakh crore and cumulative exports to about Rs. 15 lakh crore, while creating 60,000 additional direct jobs. Given that India has already manufactured approximately 125 crore mobile phones emerging as the country's second-largest mobile manufacturing ecosystem place, and now needs 99.2 percent of domestic demand through 'Made in India' devices, this scheme provides the continuity and ambition needed to sustain that momentum, while its incentives for domestic sourcing and for design and R&D by Indian brands will help deepen value addition and build genuine Indian intellectual property." Chandak added. "These decisions also validate the broader emphasis on Design and R&D. The enhanced focus on indigenous product development, industry-led R&D and innovation will help transform India from a design powerhouse into a complete design-to-manufacturing destination. Together with complementary initiatives, Semicon India 2.0 will strengthen technological self-reliance and establish India as one of the world's most trusted semiconductor and electronics manufacturing destinations, commented Mr. Navin Bishnoi, Chairperson, IESA. The programme is expected to catalyse another US\$40-50 billion in investments, create 2-3 lakh high-skilled jobs, significantly enhance domestic value addition and strengthen India's position as a trusted global semiconductor partner. Together with complementary initiatives such as the Electronics Components Manufacturing Scheme (ECMS), Production Linked Incentive (PLI), Electronics Manufacturing Clusters (EMC, 2.0) and state programmes, SEMICON 2.0 has the potential to accelerate India's journey towards nearly 40% domestic value addition in electronics manufacturing, bringing the nation significantly closer to the vision of a truly Atmanirbhar Bharat. The India Electronics and Semiconductor Association (IESA) has welcomed the launch of SEMICON India 2.0, describing it as a landmark initiative that will strengthen India's position as a trusted global semiconductor hub. Congratulating the Government of India, the Ministry of Electronics and Information Technology (MeitY) and the India Semiconductor Mission (ISM), IESA

said the programme lays the foundation for a comprehensive semiconductor ecosystem spanning research, design, manufacturing, packaging, equipment, materials and talent development. The initiative places a strong emphasis on expanding semiconductor fabrication facilities to reduce strategic import dependence, attract global investments and complete the country's semiconductor value chain. Continued support for Outsourced Semiconductor Assembly and Test (OSAT) and advanced packaging is expected to create high-value manufacturing jobs while positioning India as a preferred destination for chiplets and heterogeneous integration technologies critical for the AI era. SEMICON India 2.0 also prioritises talent development through specialised education, industry-academia collaboration and skill-building programmes aimed at creating more than two lakh skilled professionals to meet the projected global demand for nearly one million semiconductor professionals by 2030. Enhanced support under the Design Linked Incentive (DLI) Scheme is expected to accelerate indigenous chip design, intellectual property creation and semiconductor product innovation, enabling Indian startups to undertake advanced chip development programmes. The initiative further highlights the importance of industry-driven, market-oriented research and development to commercialise indigenous technologies and strengthen India's design-to-manufacturing capabilities. In addition, a dedicated focus on semiconductor equipment, specialty chemicals, gases, materials, consumables and precision engineering will deepen domestic value addition, create opportunities for MSMEs and reinforce India's role in global semiconductor supply chains. Welcoming the initiative, IESA Vice Chairperson Pradeep Vajram said the association looks forward to working closely with MeitY, the India Semiconductor Mission, state governments, industry, academia and global partners to ensure the successful implementation of SEMICON India 2.0. He said IESA will continue supporting workforce development, startup growth, industry-led R&D, policy advocacy and international collaborations to build a resilient, innovation-driven and globally competitive semiconductor ecosystem aligned with India's vision of technological leadership and Atmanirbhar Bharat.

Thank you!